

FIG. 1

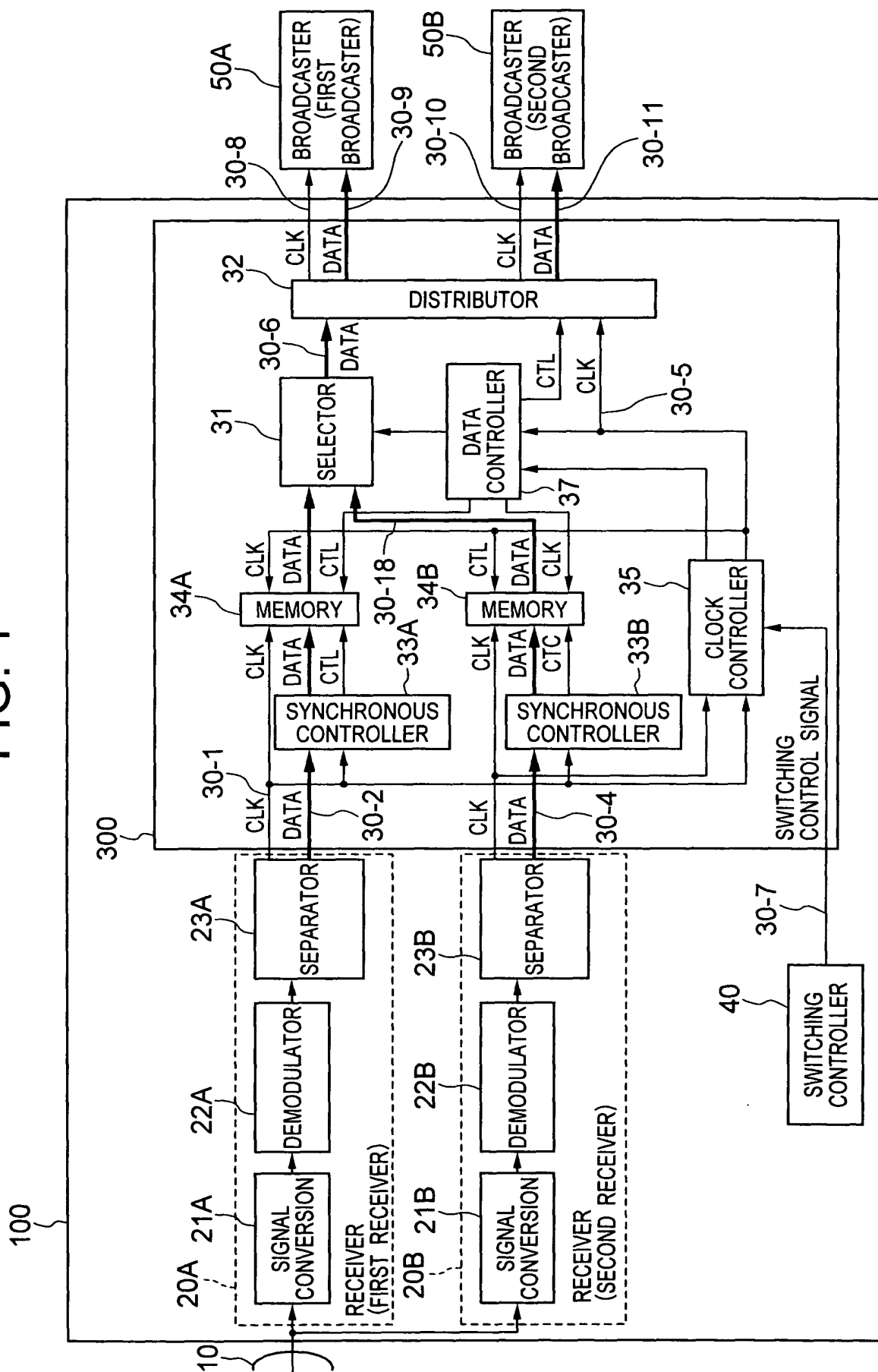
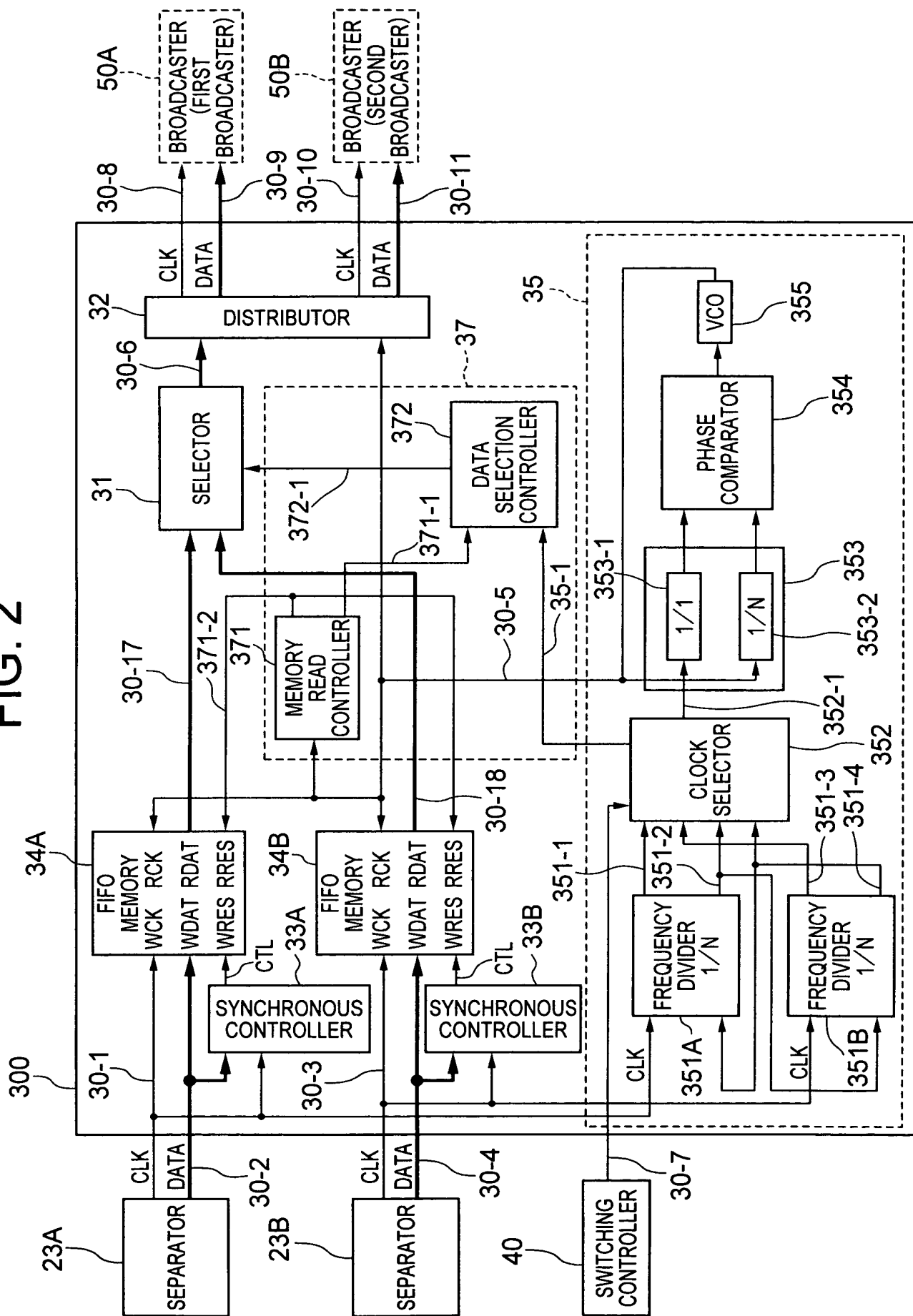
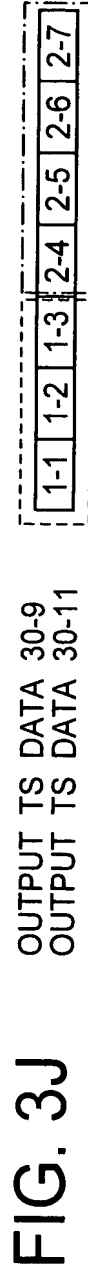
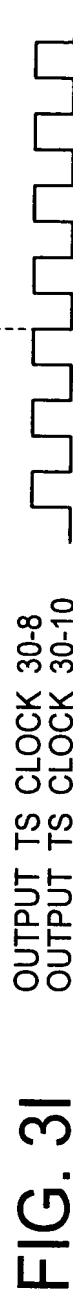
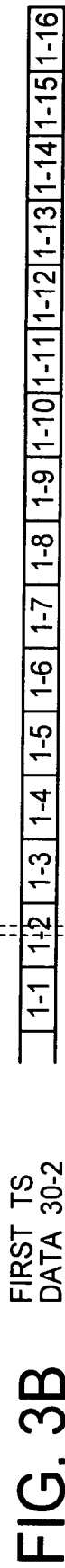
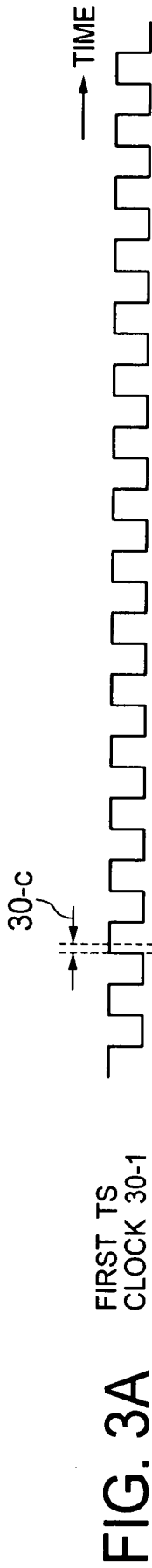


FIG. 2





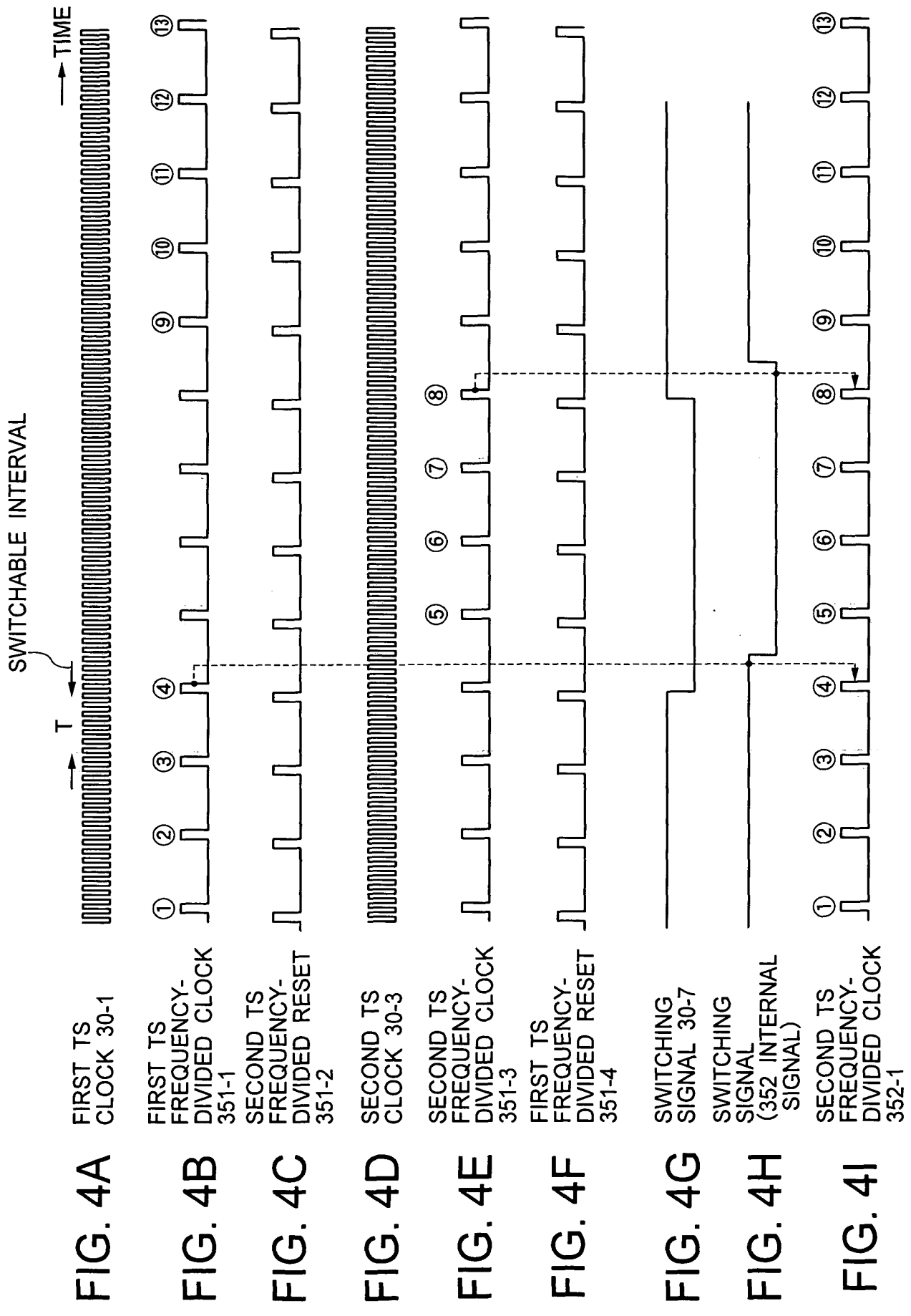


FIG. 4J VCO OUTPUT 355-1



FIG. 4K VCO FREQUENCY-DIVIDED CLOCK 353-2

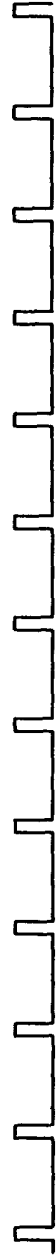


FIG. 4L CLOCK SELECTOR OUTPUT 352-1



FIG. 4M INPUT 353-1 TO PHASE COMPARATOR

